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L6 AND L10	149

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<u>L16</u>	L6 AND L10	149	<u>L16</u>
<u>L15</u>	L14 AND I10	29	<u>L15</u>
<u>L14</u>	L13 AND 250/\$.CCLS.	2466	<u>L14</u>
<u>L13</u>	(CMOS)	124274	<u>L13</u>
<u>L12</u>	L11 and L5	1	<u>L12</u>
<u>L11</u>	L10 and L6	149	<u>L11</u>
<u>L10</u>	(pixel) same (readout) near10 (multiple or many or lot adj times)	254	<u>L10</u>
<u>L9</u>	(pixel) same (readout adj multiple adj times)	0	<u>L9</u>
<u>L8</u>	(pixel adj readout adj multiple adj times)	0	<u>L8</u>
<u>L7</u>	L6 AND L5	1	<u>L7</u>